Mahatma Education Society's Pillai College of Engineering, New Panvel DEPARTMENT OF ELECTRONICS and TELECOMMUNICATION ENGINEERING

TIME TABLE IA2

CLASS: S.E. EXTC	SEM- IV

Day	Date	Time	Subject
Thursday	6th May 2021	11am-12pm	AM IV
Thursday	6th May 2021	3pm-4pm	Microcontrollers
Friday	7th May 2021	11am-12pm	LIC
Friday	7th May 2021	3pm-4pm	SAS
Saturday	8th May 2021	11am-12pm	PCE

Prof. Ameet Mehta Prof. Florence S. Dr. Avinash Vaidya (Test Coordinator) (Test Coordinator) (HOD, EXTC)

Mahatma Education Society's Pillai College of Engineering, New Panvel DEPARTMENT OF ELECTRONICS and TELECOMMUNICATION ENGINEERING

TIME TABLE IA2

CLASS: T.E. EXTC SEM- VI

Day	Date	Time	Subject
Thursday	6th May 2021	11am-12pm	MCA
Thursday	6th May 2021	3pm-4pm	CCN
Friday	7th May 2021	11am-12pm	ARWP
Friday	7th May 2021	3pm-4pm	IPMV
Saturday	8th May 2021	11am-12pm	Elective:
			DBMS

Prof. Ameet Mehta Prof. Florence S. Dr. Avinash Vaidya (Test Coordinator) (Test Coordinator) (HOD, EXTC)

Mahatma Education Society's Pillai College of Engineering, New Panvel

DEPARTMENT OF ELECTRONICS and TELECOMMUNICATION ENGINEERING

TIME TABLE IA2

CLASS: B.E. EXTC SEM- VIII

Day	Date	Time	Subject
Thursday	6th May 2021	11am-12pm	RF Design
Thursday	6th May 2021	3pm-4pm	WN
Friday	7th May 2021	11am-12pm	DLOC: SATCOM
Saturday	8th May 2021	11am-12pm	ILOC : EM

Prof. Ameet Mehta (Test Coordinator)

Prof. Florence S. (Test Coordinator)

Dr. Avinash Vaidya (HOD, EXTC)