

**Mahatma Education Society's
Pillai College of Engineering, New Panvel
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**

TIME TABLE (IA-1)

CLASS: S.E. EXTC SEM - III (A&B)

Day	Date	Time	Subject	Question Paper Marks
Saturday	24th Aug. 2024	10.00 AM to 11.30 AM	Engineering Mathematics III	40
		2.00 PM to 3.30 PM	Electronics Devices	40
Monday	26th Aug. 2024	10.00 AM to 11.30 AM	Network Theory	40
		2.00 PM to 3.30 PM	Digital System Design	40
Wednesday	28th Aug. 2024	10.00 AM to 11.00 AM	Personal Finance Management	20

TIME TABLE (IA-1)

CLASS: T.E. EXTC SEM - V (A&B)

Day	Date	Time	Subject	Question Paper Marks
Saturday	24th Aug. 2024	10.00 AM to 11.30 AM	Digital Communication	40
		2.00 PM to 3.30 PM	Image Processing & Machine Vision	40
Monday	26th Aug. 2024	10.00 AM to 11.30 AM	Embedded Systems	40
		2.00 PM to 3.30 PM	DLOC I: IOT Basics & Smart sensors	40
Wednesday	28th Aug. 2024	10.00 AM to 11.30 AM	DLOC II: Computer Communication & Network	40
			DLOC II: Database Management System	40

TIME TABLE (IA-1)

CLASS: BTech EXTC SEM - VII (A&B)

Day	Date	Time	Subject	Question Paper Marks
Saturday	24th Aug. 2024	10.00 AM to 11.30 AM	Microwave & RF Design	40
		2.00 PM to 3.30 PM	DLOC VII: AI In Neural Network	40
Monday	26th Aug. 2024	10.00 AM to 11.30 AM	DLOC VII: Advanced Network Technologies	40
Wednesday	28th Aug. 2024	2.00 PM to 3.30 PM	ILOC: E-Commerce and E-Business	40
			ILOC: Design for sustainability	40

Aboli Khedkar
19/08/24

Prof. Aboli Khedkar
(Test Coordinator)

S. Karpagavalli
19/08/24

Prof. Karpagavalli S
(Test Coordinator)

for
19/08/24

Dr. Avinash Vaidya
(HOD, EXTC)